## PART B - FEE(S) TRANSMITTAL

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**SLATER & MATSIL LLP** 17950 PRESTON ROAD SUITE 1000 **DALLAS, TX 75252** 

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10/785,140

APPLICATION NO. FILING DATE FIRST NAMED INVENTOR

Siva Chemupati

Kristin R, Hayes

ATTORNEY DOCKET NO.

Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USFTO (703) 746-4000, on the date indicated below.

CONFIRMATION NO.

(Signature)

02/24/2004

2003 P 50641 US

8178

TITLE OF INVENTION: CIRCUIT MODULE HAVING INTERLEAVED GROUPS OF CIRCUIT CHIPS

APPLIN. TYPE	SMALL ENTITY	issue pee		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
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EXA	MINER	ART UNIT	•	CLASS-SUBCLASS	1		
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1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.		Correspondence ation form e of a Customer	(1) the na or agents (2) the na registered 2 registered listed, no	nting on the patent front page, li unes of up to 3 registered pater OR, alternatively, me of a single from (having as a attorney or agent) and the nar- ed patent attorneys or agents. If name will be printed.	a member a 2	Ł Matsil, L.L.P.	
3. ASSIGNEE NAME AND	D RESIDENCE DATA TO E	E PRINTED ON TH	IE PATEN	T (print or type)			
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a. Applicant claims 5	s (from status indicated above MALL ENTITY status, See	:) 37 CFR 1.27.	Db. Appli	cant is no longer claiming SMA	LL ENTITY status. See 37 C	FR 1.27(g)(2).	
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Typed or printed name _	Ira S. Matsil		····	_ Registration	Na. 35,272		
This collection of informati an application. Confidentia submitting the completed a this form and/or suggestion Box 1450, Alexandria, Virginia Alexandria, Virginia 22313	on is required by 37 CFR 1.3 lity is governed by 35 U.S.C pplication form to the USPT a for reducing this burden, a ginia 22313-1450. DO NOT -1450.	II. The information, 122 and 37 CFR 1. O. Time will vary discuss to the CSEND FRES OR CO	is required 14. This co epending u Chief Infor IMPLETE	to obtain or retain a benefit by flection is estimated to take 12 pon the individual case. Any co mation Officer, U.S. Patent and D FORMS TO THIS ADDRESS	the public which is to file (an minutes to complete, includi maments on the amount of to Trademark Office, U.S. Det S. SEND TO: Commissioner	d by the USPTO to process) ag gathering, preparing, and ime you require to complete partment of Commerce, P.O. for Patents, P.O. Box 1450,	

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#### FACSIMILE COVER SHEET

To Commissioner for Patents

Total Pages Sent:

3

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Facsimile Number: (703) 746-4000

Transmission Date:

June 3, 2005

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chennupati

Docket No.:

2003 P 50641 US

Serial No:

10/785,140

Art Unit:

2818

Date Filed:

February 24, 2004

Title:

Circuit Module having Interleaved Groups of Circuit Chips

#### CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 746-4000 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Part B Fee Transmittal (1 original and 1 copy)

Respectfully submitted,

Kristin R. Hayes

Legal Assistant

Confirmation Respectfully Requested